

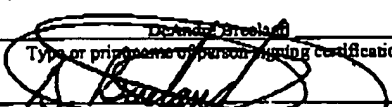
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Amendment  
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PATENT  
450100-02936

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JUL 07 2005

Applicants : Taku Yamagata, et al  
Serial No. : 09/771,743  
For : TIMING ERROR DETECTION CIRCUIT,  
DEMULATION CIRCUIT AND METHODS  
THEREOF  
Filed : January 29, 2001  
Examiner : Pathak, Sudhanshu C.  
Art Unit : 2634  
Confirmation No. : 1150

745 Fifth Avenue  
New York, NY 10151  
(212) 588-0800

<p><b><u>FACSIMILE</u></b></p> <p>I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office, Facsimile No. 703-872-9306, on July 7, 2005.</p> <p>Defender: Brooklyn Type or print name of person signing certification  Signature July 7, 2005 Date of Signature</p>
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**AMENDMENT UNDER 37 C.F.R. § 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed on May 18, 2005, having a three-month statutory period for response set to expire on August 18, 2005, Applicants respectfully request that the Examiner amend the above-identified application as follows.